

**Description**

The DigiFlex Performance (DP) Series digital servo drives are designed to drive brushed and brushless servomotors. These fully digital drives operate in torque, velocity, or position mode and employ Space Vector Modulation (SVM), which results in higher bus voltage utilization and reduced heat dissipation compared to traditional PWM. The command source can be generated internally or can be supplied externally. In addition to motor control, these drives feature dedicated and programmable digital and analog inputs and outputs to enhance interfacing with external controllers and devices.

This DP Series drive features a CANopen interface for networking and a RS-232 interface for drive configuration and setup. Drive commissioning is accomplished using DriveWare, available at www.a-m-c.com.

All drive and motor parameters are stored in non-volatile memory.

**Power Range**

Peak Current	15 A (10.6 A <sub>RMS</sub> )
Continuous Current	10 A (7.1 A <sub>RMS</sub> )
Supply Voltage	40 - 190 VDC



**Features**

- ▲ Four quadrant regenerative operation
- ▲ Space vector modulation (SVM) technology
- ▲ Fully digital state-of-the-art design
- ▲ Programmable gain settings
- ▲ Fully configurable current, voltage, velocity and position limits
- ▲ PIDF velocity loop
- ▲ PID + FF position loop
- ▲ Compact size, high power density

**MODES OF OPERATION**

- Current
- Position
- Velocity

**COMMAND SOURCE**

- ±10 V Analog
- 5V Step & Direction
- Communication Interface

**FEEDBACK SUPPORTED**

- Resolver
- ±10 V Analog
- Auxiliary Incremental Encoder

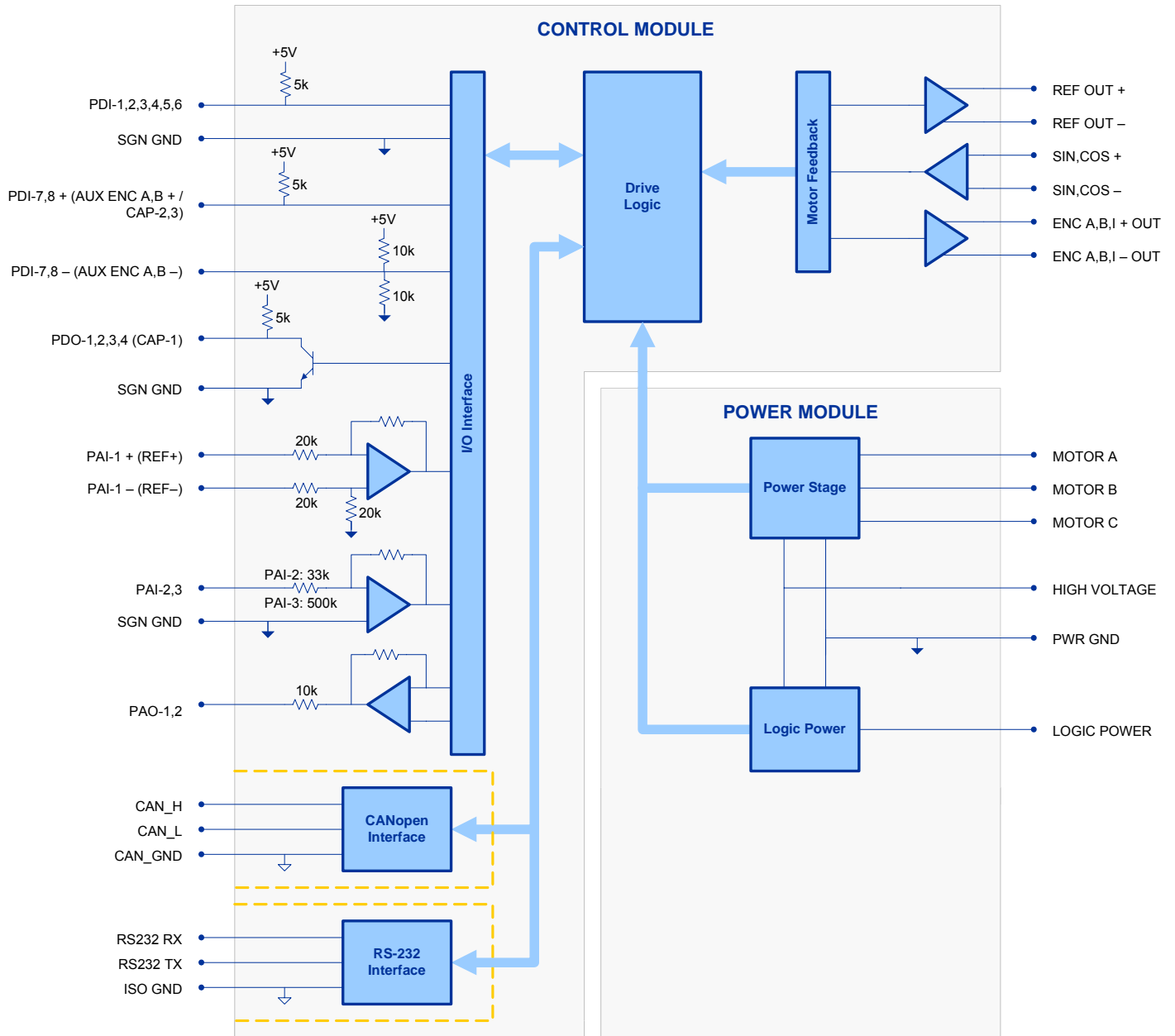
**INPUTS/OUTPUTS**

- 3 High Speed Captures
- 3 Programmable Analog Inputs
- 2 Programmable Analog Outputs
- 2 Programmable Digital Inputs (Differential)
- 6 Programmable Digital Inputs (Single-Ended)
- 4 Programmable Digital Outputs




**COMPLIANCES & AGENCY APPROVALS**

- RoHS
- UL/cUL Pending
- CE Pending

**BLOCK DIAGRAM**



**Approvals and Compliances**

	<p>US and Canadian safety compliance with UL 508c, the industrial standard for power conversion electronics. UL registered under file number E140173. Note that machine components compliant with UL are considered UL registered as opposed to UL listed as would be the case for commercial products.</p>
	<p>Compliant with European CE for both the Class A EMC Directive 89/336/EEC on Electromagnetic Compatibility (specifically EN 61000-6-4:2001, EN 61000-6-2:2001, EN 61000-3-2:2000, and EN 61000-3-3:1995/A1:2001) and LVD requirements of directive 73/23/EEC (specifically EN 60204-1), a low voltage directive to protect users from electrical shock.</p>
	<p>RoHS (Reduction of Hazardous Substances) is intended to prevent hazardous substances such as lead from being manufactured in electrical and electronic equipment.</p>

**SPECIFICATIONS**

Power Stage Specifications		
Description	Units	Value
DC Supply Voltage	VDC	40 - 190
Over Voltage Limit	VDC	198
Under Voltage Limit	VDC	35
Peak Output Current	A	15
Maximum Continuous Output Current	A	10
Maximum Continuous Output Power	W	1900
Maximum Power Dissipation at Continuous Current	W	95
Minimum Load Inductance (Line-To-Line) <sup>1</sup>	μH	250
Switching Frequency	kHz	20
Low Voltage Supply Outputs	-	+5 VDC (250 mA)
Control Specifications		
Description	Units	Value
Communication Interfaces	-	CANopen (ISO 11898-2), RS-232
Command Sources	-	±10 V Analog, 5V Step & Direction, Communication Interface
Feedback Supported	-	±10 V Analog, Auxiliary Incremental Encoder, Resolver
Commutation Methods	-	Sinusoidal
Modes of Operation	-	Current, Position, Velocity
Motors Supported	-	Brushed, Brushless, Induction, Voice Coil
Hardware Protection	-	40+ Configurable Functions, Over Current, Over Temperature (Drive & Motor), Over Voltage, Short Circuit (Phase-Phase & Phase-Ground), Under Voltage
Programmable Digital Inputs/Outputs (PDIs/PDOs)	-	8/4
Programmable Analog Inputs/Outputs (PAIs/PAOs)	-	3/2
Current Loop Sample Time	μs	50
Velocity Loop Sample Time	μs	100
Position Loop Sample Time	μs	100
Mechanical Specifications		
Description	Units	Value
Size (H x W x L)	mm (in)	132.5 x 89.5 x 35.9 (5.2 x 3.5 x 1.4)
Heatsink (Base) Temperature Range <sup>2</sup>	°C (°F)	0 - 65 (32 - 149)
Storage Temperature Range	°C (°F)	-40 - 85 (-40 - 185)
Cooling System	-	Natural Convection
Form Factor	-	Stand Alone
IP Rating	-	IP10
AUX COMM Connector	-	3-pin, 2.5 mm spaced, enclosed, friction lock header
COMM Connector	-	Shielded, dual RJ-45 socket with LEDs
FEEDBACK Connector	-	15-pin, high-density, female D-sub
I/O Connector	-	26-pin, high-density, female D-sub
POWER Connector	-	6-pin, 3.96 mm spaced, friction lock header

**Notes**

1. Low inductance motors, such as 'pancake' and 'basket-wound', require external inductors. The Minimum Load Inductance provided assumes the highest allowed bus voltage. Lower inductances are acceptable for lower bus voltages.
2. Additional cooling and/or heatsink may be required to achieve rated performance.

**PIN FUNCTIONS**

AUX COMM - RS232 Communication Connector			
Pin	Name	Description / Notes	I/O
1	RS232 RX	Receive Line (RS-232)	I
2	RS232 TX	Transmit Line (RS-232)	O
3	ISO GND	Isolated Signal Ground	IGND

COMM - CAN Communication Connector			
Pin	Name	Description / Notes	I/O
1	CAN_H	CAN_H Line (Dominant High)	I
2	CAN_L	CAN_L Line (Dominant Low)	I
3	CAN_GND	CAN Ground	CGND
4	RESERVED	Reserved	-
5	RESERVED	Reserved	-
6	RESERVED	Reserved	-
7	CAN_GND	CAN Ground	CGND
8	RESERVED	Reserved	-

FEEDBACK - Feedback Connector			
Pin	Name	Description / Notes	I/O
1	RESERVED	Reserved	-
2	RESERVED	Reserved	-
3	RESERVED	Reserved	-
4	REF OUT +	Resolver Reference/Excitation Output	O
5	REF OUT -		O
6	SIN+	Resolver Sine Input	I
7	SIN-		I
8	COS+	Resolver Cosine Input	I
9	COS-		I
10	RESERVED	Reserved	-
11	RESERVED	Reserved	-
12	SGN GND	Signal Ground	SGND
13	+5V OUT	+5V Encoder Supply Output (Short Circuit Protected)	O
14	PAI-3	Programmable Analog Input	I
15	RESERVED	Reserved	-

I/O - Signal Connector			
Pin	Name	Description / Notes	I/O
1	PDO-1	Programmable Digital Output	O
2	SGN GND	Signal Ground	SGND
3	PDO-2	Programmable Digital Output	O
4	PAI-1 + (REF+)	Differential Programmable Analog Input or Reference Signal Input	I
5	PAI-1 - (REF-)		I
6	PAI-2	Programmable Analog Input	I
7	PAO-1	Programmable Analog Output	O
8	PAO-2	Programmable Analog Output	O
9	PDI-8 - (AUX ENC B-)	Programmable Digital Input or Auxiliary Encoder (For Differential Signals Only)	I
10	PDO-3	Programmable Digital Output	O
11	PDI-1	Programmable Digital Input	I
12	PDI-2	Programmable Digital Input	I
13	PDI-3	Programmable Digital Input	I
14	PDO-4	Programmable Digital Output	O
15	+5V OUT	+5V Encoder Supply Output (Short Circuit Protected)	O
16	SGN GND	Signal Ground	SGND
17	PDI-7 + (AUX ENC A+ / CAP-2)	Programmable Digital Input or Auxiliary Encoder or High Speed Capture	I
18	PDI-8 + (AUX ENC B+ / CAP-3)	Programmable Digital Input or Auxiliary Encoder or High Speed Capture	I
19	PDI-4 (CAP-1)	Programmable Digital Input or High Speed Capture	I
20	PDI-5	Programmable Digital Input	I
21	PDI-6	Programmable Digital Input	I
22	SGN GND	Signal Ground	SGND
23	RESERVED	Reserved	-
24	RESERVED	Reserved	-
25	RESERVED	Reserved	-
26	PDI-7 - (AUX ENC A-)	Programmable Digital Input or Auxiliary Encoder (For Differential Signals Only)	I

POWER - Power Connector			
Pin	Name	Description / Notes	I/O
1	MOTOR A	Motor Phase A	O
2	MOTOR B	Motor Phase B	O
3	MOTOR C	Motor Phase C	O
4	HIGH VOLTAGE	DC Power Input	I
5	PWR GND	Power Ground (Common With Signal Ground)	PGND
6	LOGIC PWR	Logic Supply Input	I

## HARDWARE SETTINGS

### Switch Functions

Switch	Description	Setting	
		On	Off
1	Bit 0 of binary value of drive address/ID.	1	0
2	Bit 1 of binary value of drive address/ID.	1	0
3	Bit 2 of binary value of drive address/ID.	1	0
4	Bit 3 of binary value of drive address/ID.	1	0
5	Bit 4 of binary value of drive address/ID.	1	0
6	Bit 5 of binary value of drive address/ID.	1	0
7	Bit 0 of binary value of drive bit rate setting.	1	0
8	Bit 1 of binary value of drive bit rate setting.	1	0

### Additional Details

The drive can be configured to use the address and/or bit rate stored in non-volatile memory by setting the address and/or bit rate value to 0. Use the table below to map actual bit rates to a bit rate setting.

Bit Rate (kbits/sec)		Value For Bit Rate Setting
CANopen	RS-485	
Load from non-volatile memory	Load from non-volatile memory	0
500	9.6	1
250	38.4	2
125	115.2	3

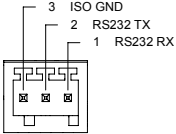
### Jumper Settings

Jumper	Description	Configuration		
		Not Installed	Pins 1-2	Pins 2-3
J1	Reserved.	-	-	N/A
J2	CAN bus termination. Install this jumper on the last drive in a CAN network.	Non-terminating Node	Terminating Node	N/A

**MECHANICAL INFORMATION**

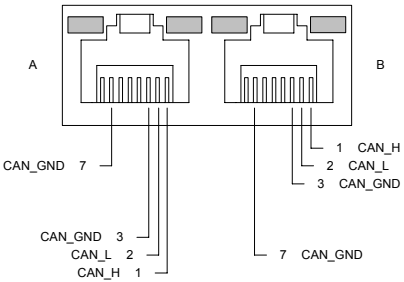
**AUX COMM - RS232 Communication Connector**

Connector Information	3-pin, 2.5 mm spaced, enclosed, friction lock header
Mating Connector	Phoenix: Plug P/N 1881338



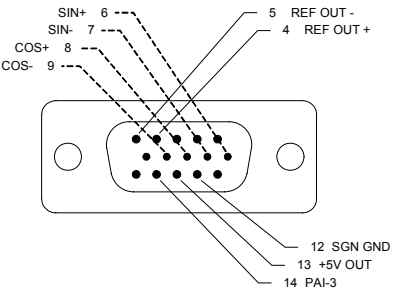
**COMM - CAN Communication Connector**

Connector Information	Shielded, dual RJ-45 socket with LEDs
Mating Connector	AMP: Plug P/N 5-569552-3



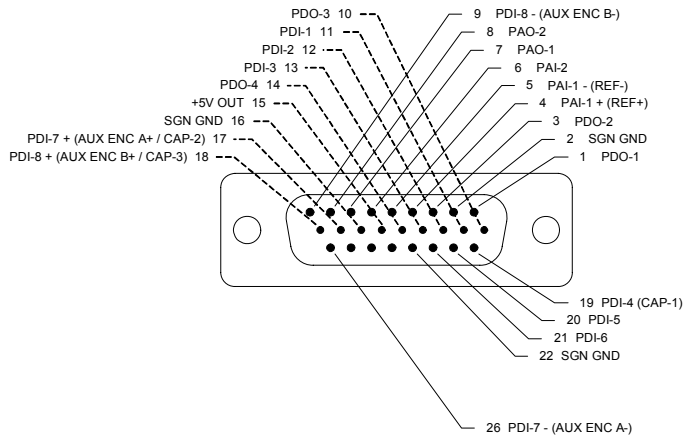
**FEEDBACK - Feedback Connector**

Connector Information	15-pin, high-density, female D-sub
Mating Connector	AMP: Plug P/N 748365-1; Housing P/N 748677-1; Terminals P/N 748333-4 (loose) or 748333-2 (strip)



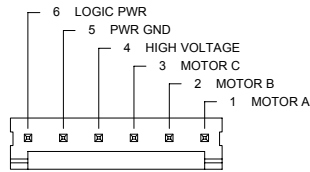
**I/O - Signal Connector**

Connector Information	26-pin, high-density, female D-sub
Mating Connector	AMP: Plug P/N 748365-1; Housing P/N 748677-2; Terminals P/N 748333-4 (loose) or 748333-2 (strip)



**POWER - Power Connector**

Connector Information	6-pin, 3.96 mm spaced, friction lock header
Mating Connector	AMP: Plug P/N 770849-6; Terminals P/N 770522-1 (loose) or 770476-1 (strip)



**PART NUMBERING INFORMATION**

Example: **D P R A N I E - 0 1 5 A 4 0 0 -**

Drive Series	
DP	DigiFlex Performance

Communication	
R	RS232/RS485
C	CANopen or RS232
Q	SynqNet

Command Inputs	
AN	Analog (±10V) No Step & Direction
AL	Analog (±10V) Low Voltage Step & Direction (5V)
AH	Analog (±10V) High Voltage Step & Direction (24V)
NL	No Analog Low Voltage Step & Direction (5V)
NN	No Analog, No Step & Direction (Communication Interface Only)

Digital I/O	
I	Isolated (24V)
T	TTL (5V) Non-Isolated

Motor Feedback	
E	Incremental Encoder and/or Halls
R	Resolver
A	Absolute Sin/Cos (Hiperface & Endat)
S	Sin/Cos with Halls

Customer Special	
Code used to identify customer specials	

Revision	
A through Z (letters may be skipped)	

Max DC Bus Voltage (V <sub>DC</sub> )	
080	80
200	200
400	400
600	600

Power and Logic Supply	
A	AC Input +24V <sub>DC</sub> User Logic Supply Required
N	AC Input Only No Logic Supply Required (Internal Supply)
B	DC Input Both Logic Supply Options (Internal or User)
L	DC Input Logic Supply Required
D	DC Input Only Internal Logic Supply

Peak Current (A <sub>O (10 Peak)</sub> )	
015	15
016	16
020	20
025	25
030	30
040	40
060	60
100	100

DigiFlex® Performance™ series of products are available in many configurations. All models listed on the website are readily available, standard product offerings. Other combinations or possibilities can be made available for OEMs with volume requests of 100 or more. Contact Applications Engineering for further information and details.

All specifications in this document are subject to change without written notice. Actual product may differ from pictures provided in this document.